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# Added technical aspects of p4est: <br> Alternative quadrant representation and MPI-3 shared memory 

## Introduction

Many numerical simulations requires a mesh of computational cells covering the domain of interest. The solution is approximated by functions associated with a set of cells.

- Implementation: the p4est software library
- Dynamic management of adaptive octrees
- Discretization of a computational domain
- Efficiently manages large-scale parallel tasks


Typical workflow of the p4est software library.
The p4est library is actively used worldwide: linked e.g. by solver libraries deal.ii, PETS ForestClaw. Some possible applications: continuum mechanics and particle simulation.


A representation of a refined mesh built by p4est within ForestClaw ${ }^{71}$ on a torus.


Modelled advection problem performed by ForestClaw ${ }^{1}$ solver.

## Alternative quadrant representations

## Cells as quadrants

p4est represents cells with 2D squares (3D cubes) which we call quadrants (octants). Their characterization:

- Defined by the coordinates of a corner and a level.
- It is allowed for them to be of various sizes.
- Store user's information depending on application


Per-quadrant operations are listed in the original paper on p4est [1] and the source code.
*r = _mm_sub_epi32.
Some quadrant properties:
mm_and_si128 (*q, _mm_set_epi32 (~QUAD_LEN (level) ~QUAD_LEN ( level) ~QUAD_LEN (level) 0xFFFFFFFF)) mm_set_epi32 (0, 0, 0, 1));

Implementation of Parent algorithm, constructing parent r of the 128 -bit quadrant q . Written with use AVX/SSE.

Form a disioint union of all leaves in a forest. Partitioned btw. MPI processes by space filling curve (SFC) order.

- SFC is aka Morton or Z-curve [2, 3].
- Quadrants can be set by $\ell$ and either $(x, y, z)$ or Morton index id.

Since an octant is defined by $x, y, z$ and $\ell$, we consider four-way SIMD (Single Instruction Multiple Data) for accelerated processing. We base new quadrant representation on the Advanced
Vector Extensions/Streaming SIMD Extensions (AVX/SSE).


| $l_{7}$ | $l_{6}$ | $\cdots$ | 0 | $z_{30}$ | $\cdots$ | $z_{0}$ | 0 | $y_{30}$ | $\cdots$ | $y_{0}$ | 0 | $x_{30}$ | $\cdots$ | $x_{0}$ | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

These intrinsics operate on extended processor registers. Specifically, we chose the special SSE2 type _m128i, that stores 128 bits of data interpreted as signed integers.

## Shared memory algorithm: Partition

The Partition algorithm for redistributing work-load guarantees similar amounts of work for all computation nodes. Current version of the Partition, as published in [1] and [4], asynchronously sends and receives the data. We aim to eliminate the redundancy of the sends within each node with shared memory (SM) windows introduced in MPI 3.0.


Partition over non-contiguous shared memory.


Partition over contiguous shared memory.

Distribution before and after Partition.

We use the following notation:
$N$ global and $N_{p}$ local to process $p$ number of quadrants in a forest.

- Element offsets $O_{p}: O_{p+1}-O_{p}=N_{p}$

Highlights of approaches for Partition:

- In each case we calculate new offsets $O_{p}^{\prime}$ to define new boundaries on SFC Classical Partition relies on asyn chronous ISend and IRecv calls. - With MPI-3 we allocate new SM and perform simply copying into it.
Contiguous shared memory allows reassigning new element offsets $O_{p}^{\prime}$.


Rack 1

## Partition: numerical results

We run mesh partition test for various combinations of algorithm and quadrant implementations. The performance scalability was tested on a desktop PC with up to 36 physical cores split between two sockets. We modeled an unbalanced mesh with approximately 3 million quadrants per core shipping $80 \%$ of them, which is equal to sending 2.5 GB of data.


Conclusions and highlights: Quadrants with Morton index id demonstrate the fastest results over others. BUT contribute to changes of quadrant bit operations. Contiguous shared memory shows better performance over non-contigious

- We consider the pair of AVX/contiguous as a major user's selection.


## References

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2023 University of Bonn, Hausdorff Center for Mathematics, Institute for Numerical Simulation. This work is supported by a scholarship of the German Academic Exchange Service (DAAD). kirilin@ins.uni-bonn.de

